**Title:**

Design of Control system of an Automatic Washing Machine using Verilog.

**Abstract:**

The control system of an Automatic Washing machine is implemented using Finite State Machine. The control system generates the control signal to control the overall operation of the washing machine. This project incorporates basic functionalities such as soak, wash, rinse & dry. For these functionalities different times for each functionality are scheduled. As the real time applications, we know we wash the clothes as per weight basis for these different modes of operations are designed. As per the modes the washing of clothes is done differently i.e., it uses different times for different operations.

**Outcomes:**

A real time automatic washing machine controller is developed using Verilog.

All functionality is designed and verified.

Application of theoretical knowledge in Hands-on project, understanding through practical experience.